



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of)

ELIYAHOU HARARI, ROBERT NORMAN,)
and SANJAY MEHROTRA)

Serial No.: 08/999,498)

Filed: December 29, 1997)

For: MULTI-STATE FLASH EEPROM)
SYSTEM WITH SELECTIVE)
MULTI-SECTOR ERASE)
(as amended))

Group Art Unit: 1714

San Francisco, California

Assistant Commissioner for Patents
Washington, D.C. 20231

By Express Mail #: EM215669450US

Dated: April 7, 1998

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GROUP 2500

SECOND PRELIMINARY AMENDMENT TRANSMITTAL

Sir:

Transmitted herewith is a Second Supplemental Amendment in the captioned application, accompanied by U.S. Patent No. 5,619,451. A check in the amount of \$460.00 is enclosed to cover the fee for filing additional claims.

The Commissioner is hereby authorized to charge any additional fees which may be required, or credit any overpayment, to Deposit Account No. 13-1030. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

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Dated: April 7, 1998

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Atty. Docket: HARI.006USH

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APR 30 1998

SECOND PRELIMINARY AMENDMENT

Sir:

Please amend the above-identified patent application as follows:

IN THE SPECIFICATION:

Page 1, between lines 3 and 4, amend the "Cross-Reference to Related Applications" section added by the Preliminary Amendment filed with this continuation application, to insert the following patent number for the blanks at line 3 of the added section: -- 5,719,808--.

IN THE CLAIMS:

Add the following new claims:

--90. A method for erasing a non-volatile electrically erasable and programmable integrated circuit memory, comprising the steps of:

unlocking sectors of said memory to be erased;
applying an erasure pulse to said unlocked sectors;
detecting cells in each sector until an unerased cell is detected;